

REMARKS

Claims 1-33 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By a separate letter, a proposed drawing correction has been submitted.

It is respectfully submitted that the proposed drawing correction obviates the objections to the drawings.

Claims 12-13, 25-26, and 23 were objected to under 37 CFR 1.75(c), as being improper dependent form.

This objection is respectfully traversed.

The Examiner alleges that Claim 12 does not further limit the subject matter of Claim 1.

The addition of the second system in combination with the first system further limits the Claim 1 since this addition includes additional elements.

With respect to Claim 13, the Examiner alleges that this claim does not further limit the subject matter of Claim 1. This claim includes a capacitor multiplier. Since the capacitor multiplier includes additional elements is more narrower than the subject matter of Claim 1.

The additional limitations of Claim 25, 26, and 33 limit the subject matter of the claims from which it depends.

It is respectfully submitted that Claims 1-33 are free from being of improper dependent form.

Claim 7, 8, 12, 20, 25, and 26 were objected to because of informalities.

By the instant amendment, these claims have been amended to eliminate the informalities noted by the Examiner.

It is respectfully submitted that Claims 1-33 are free from informalities.

Applicants appreciate the indication that Claims 1-33 are allowed subject to the informalities and objections noted in the Office Action.

In light of the above, it is respectfully submitted that these informalities and objections have been overcome and Claims 1-33 are in condition for allowance.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'W. Daniel Swayze, Jr.', written in a cursive style.

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the claims:

Claims 7, 8, 12, 20, 25, and 26 have been amended as follows:

7. The system of claim 3, each of the first and second MOSFETs further comprising a respective PMOS transistor.

8. The system of claim 3, each of the first and second MOSFETs further comprising a respective NMOS transistor.

12. The system of claim 1 defining a first system for extracting a threshold voltage, further comprising a second system for extracting a threshold voltage coupled to the first system for extracting a threshold voltage to provide a stacked threshold voltage extraction system having an output that is an integer multiple of the threshold voltage of the second system.

20. The system of claim 16, the first and second parts of the voltage divider comprising first and second capacitors respectively.

25. The system of claim 16 defining a first system for extracting a threshold voltage and further comprising a second system for extracting a threshold voltage coupled to the first system for extracting a threshold voltage to provide a stacked threshold voltage extraction system having an output that approximates an integer multiple of the threshold voltage of the second system.

26. The system of claim 16 in combination with a capacitor multiplier circuit, the combination comprising:

the capacitor multiplier circuit comprising first and second amplifier stages coupled together at a common node, the first stage having a first input that receives a bias current, ~~and an AC feedback network coupled between an output of the second stage of~~

~~the capacitor multiplier circuit, the output of the second stage of the capacitor multiplier circuit being coupled to the common node; and~~

the output voltage from the threshold voltage extraction system being applied to the capacitor multiplier circuit so that voltage approximately equal to the threshold voltage is at the common node, such that a startup offset for the capacitor multiplier circuit is mitigated as the bias current is applied to the first input of the capacitor multiplier circuit.